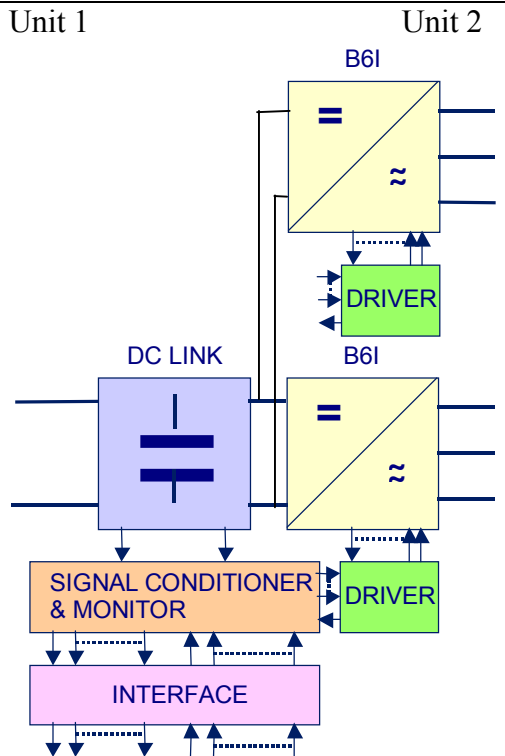


Key Data

2 x 3 x 450A AC at 500V AC, water cooled

General Information

Stack with IGBT's, heatsinks, capacitors, drivers and sensors for several inverter applications. These are only technical data! Please read heedful the complete documentation and attend the adopted design environment! Especially the EMC environment and the controller functionality are important.

Topology	DC Link + 2B6I		
Load Type	Resistive, inductive load		
Cooling	Water		
Target Application	Industrial Drive		
Extra	Available in Master [M] or Slave [S] Configuration		
Driver core	Scale Driver		
Monitors	Current-, Voltage-, Temperature-Monitoring		
Module (Unit1)		n.a.	
Module (Unit2)	IGBT	6x FF800R12KL4C	
Interface	Electrical, opt. optical		
Standards	EN50178, UL94, prepared for UL508C		
Product ID (eupec)			
Drawing No.	38000002_B_MB		
Circuit Diagram No.	57000002		

Electrical Data

	Parameter		Min.	Typical	Max.	
Dedicated Line Voltage	For Isolation Management	VLine		500		VRMS
DC Link Voltage		VDC		675	880	Vav
DC Link Over Voltage Shutdown	Within 100µs			VDCmax		V
DC Link Current	At IUnit2, VUnit2, cos φUnit2	IDCLink Input		1116		Aav
Voltage Unit1		VUnit1		-		VRMS
Continuous Current Unit1	ϑ=ϑair_inlet	IUnit1			-	ARMS
Short-time Current Unit1	10s, every 180s, initial load = IUnit1	IUnit1_10			-	ARMS

Pulse Current Unit1	Sine half wave 20ms				-	A _{peak}
DC Current at Unit1	No rotating field, $\vartheta = \vartheta_{\text{air inlet}}$	I _{Unit1_DC}			-	A _{av}
Over Current Shutdown Unit1	Percentage of I _{Unit1} . within 15 μ s				-	%
Switching Freq. Unit1		f _{sw1}			-	Hz
Power Losses Unit1	$V = V_{\text{unit1_min}}$, $I = I_{\text{Unit1}}$, f _{sw} =f _{sw1}	P _{loss1}			-	W
Voltage Unit2	Depending on Controller	V _{Unit2}		500		V _{RMS}
Displacement factor		cos_φ _{Unit2}	-0,9		+0,9	
Continuous Current Unit2	$\vartheta = \vartheta_{\text{water inlet}}$, $\vartheta_{\text{chip}} \leq 125^{\circ}\text{C}$ f _{Unit2} >5Hz both B6I separated	I _{Unit2}			2x3x 450	ARMS
Short-time Current Unit2	$\vartheta_{\text{water inlet}} \leq 40^{\circ}\text{C}$, 10s, every 180s, initial load = I _{Unit2}	I _{Unit2_10}			2x3x 540	ARMS
Pulse Current Unit2	Sine half wave 20ms, starting from I _{Unit2} .	I _{Unit2peak}			-	A _{peak}
DC Current at Unit2	No rotating field, $\vartheta = \vartheta_{\text{air inlet}}$	I _{Unit2_DC}			0,4* I _{Unit2}	ADC
Over Current Shutdown Unit2	Percentage of I _{Unit2} . within 15 μ s			125		%
Switching Freq. Unit2		f _{sw2}			3000	Hz
Power Losses Unit2	$I = I_{\text{Unit2}}$, f _{sw} =f _{sw2}	P _{loss2}		8300		W
Power Losses (PCB and Capacitor)		P _{loss_aux}			315	W
Filter resistors at Output Unit2	Applicable for Sine wave filters (damping, optionally)	R _{Filter}			-	Ohm
		PR _{Filter}			-	Watt
Auxiliary Voltage		V _{aux}	18	24	30	V _{av}
Auxiliary Power Demand	V _{aux} =24 V _{av} , to feed with B6U	P _{aux}		80		W
EMC Test	According EN61800-3 at named interfaces	Power	V _{Burst}		2	kV
		Control	V _{Burst}		1	
		Aux (24V)	V _{Surge}		1	kV
Insulation Test Voltage	According EN50178 f=50Hz, t=1min	V _{isol}		1,8		kV _{RMS}

Important Component Data

DC Link Capacitor		CDC		23,5		mF
DC Link Capacitor		Type	Elcap			
Capacitor Design Lifetime	Load cycle for: Wind	LTD		-		Year
	Load cycle for: Solar	LTD		-		Year

(eupec approximation)	Load cycle for: Industrial Drive ($\vartheta_{air}=35^{\circ}\text{C}$, $I=0,8$, Duration=24h/Day)	LTD		6		Year
-----------------------	--	-----	--	---	--	------

Requirements to the Power Source

Assumed Inductance Of Feeding Power Source	(Necessary inductance not included, feeded by B6U)	LFeed		70		μH
--	--	-------	--	----	--	---------------

Fan Data (requirements when not implemented)

Fan Type	Assumed			-		
Fan Voltage		VFan		-		VRMS
Fan Frequency		fFan		-		Hz
Fan Current		IFan		-		ARMS
Fan Air Pressure	Assumed	Δp_{AirFan}		-		Pa

Controller Interface Data

Driver	See Data Sheets	PCB	TR10x			
Paralleling Interface	Master (only first B6I)	PCB	SAD101			
	Slave See Data Sheet		-			
Optical Interface	Master (only first B6I) (optionally)	PCB	OEA101			
	Slave See Data Sheet		-			
Digital Input Level	Resistor to Gnd (1,8k) High = on min 15mA	Vin	0		15	V
Digital Output Level	Open collector Low = ok max 15mA	Vout	0		15	V
Analog Current Outputs Unit1	Load max 1mA at IUnit1			-		V
Analog Current Outputs Unit2	Load max 1mA at IUnit2			4		V
Analog DC Link Voltage Output	Load max 1 mA At VDCmax	VDCout		9		V
Analog Temperature Out	Load max 1mA At $\vartheta_j=125^{\circ}\text{C}$	V ϑ out		9		V
Optical Input Level	optionally		12			μW
Optical Output Level	optionally				60	μW

Requirements to the Controller

EMC Protection	According EN61800-3 at auxiliary power and control interface		1			kV
----------------	--	--	---	--	--	----

EMC Environment			Shielding concept with TE (True Earth) separated from PE, HF conform installation			
Drive Pulse Time		ton_min	10			μs
Blockout Time		tpause	10			μs
Over Voltage Shutdown Reaction Time	After over voltage message by ModSTACK interface				50	μs
Over Current Shutdown Reaction Time	After over current message by ModSTACK interface				10	μs

Mechanical Data

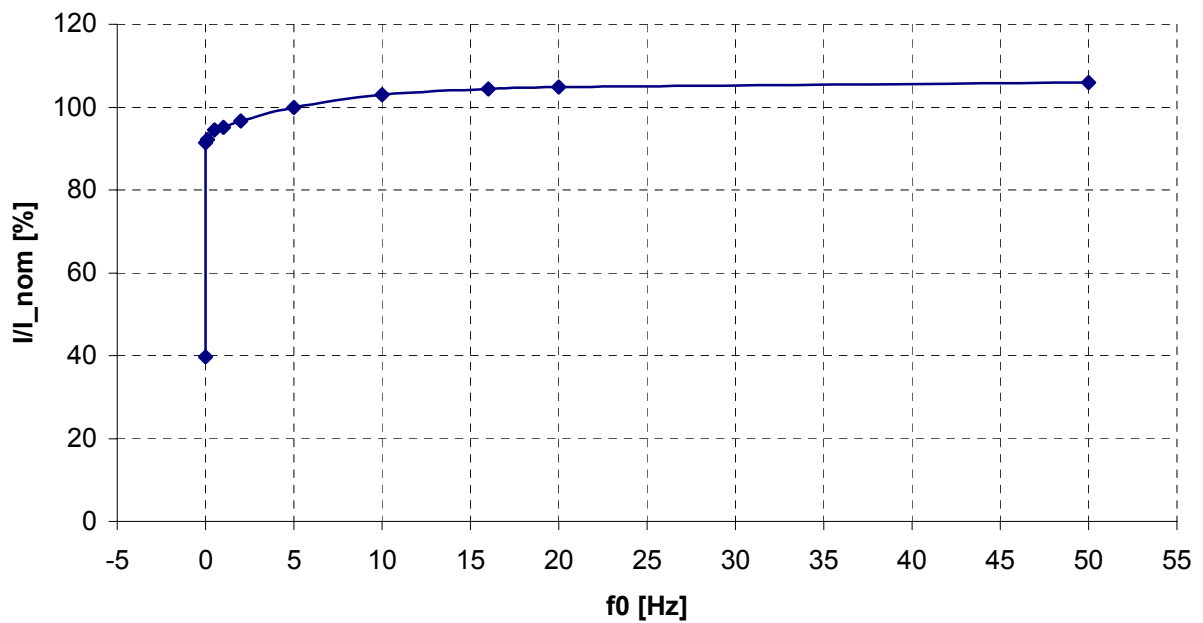
Air Velocity	ϑ _{Air} =20°C P _{Air} =1013 hPa Dry- and dust free, measured outside of heatsink. According DIN 41882	v _{Air}	-			m/s
Air Flow/ heatsink		dV/dt _{Air}	-			m ³ /h
Air Pressure Drop/ heatsink		Δp _{Air}		-		Pa
Water Flow/ heatsink	According Coolingwater Specification from eupec for copper tubes	dV/dt _{Water}	16			l/min
Water Pressure Drop/ heatsink		Δp _{Water}		200		mbar
Max. Water Pressure				8		bar
Water connection (Tube diameter)				¾"		Inch
Dimensions	Width x Depth x Height		1090	596	250	mm
Mass	Approximation			101		kg
Storage Temperature Range		ϑ _{stor}	-40		+65	°C
Operating Temperature range (PCB and Capacitor)	Minimal 0 °C for optional optical interface	ϑ _{op}	-25 (0)		+55	°C
Cooling Air Inlet Temperature (Heatsink)		ϑ _{air_inlet}	-		-	°C
	Heatsink temperature > -25°C		-			
Cooling Water Inlet Temperature (Heatsink)		ϑ _{water_inlet}	-25		+40	°C
Cooling Air Velocity (PCB and Capacitor)		v _{Air_PCB}	2			m/s
Air Pressure	Standard atmosphere	p _{Air}	900		1100	hPa
Humidity	No Condensation	Rel. H	0		95	%
Installation Height			0		1000	m
Vibration	EN60068-2-6, Fc 10..59Hz 0,075mm				10	m/s ²

Permanence Vibration	EN60068-2-6, Fc 10-150Hz, 20 Cycles				20	m/s ²
Shock	EN60068-2-27, Ea Halfsine 11ms, 3 pulses				100	m/s ²
Protection Degree			IP00			
Pollution Degree			2			
Over Voltage Category			III			

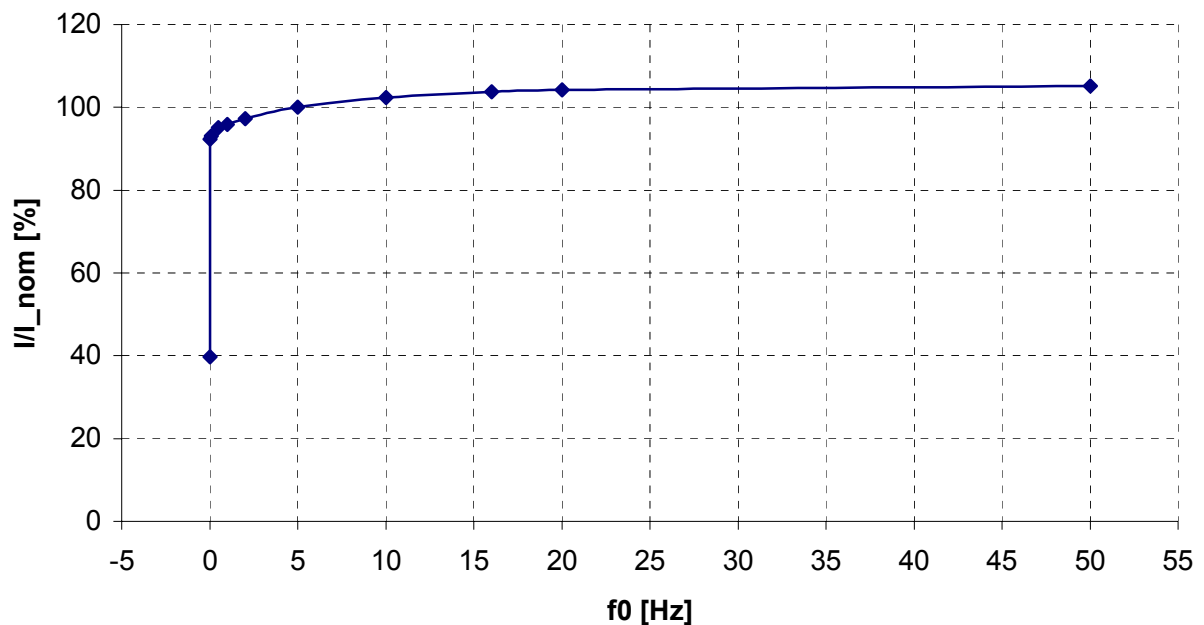
Derating Curves (IGBT Part)

Current derating at low rotating field frequency (f_0). **Maximal 100% current is allowed.**

$\cos(\phi) = 0.64$, (motor)
 $\Theta_{air} = 40^\circ\text{C}$

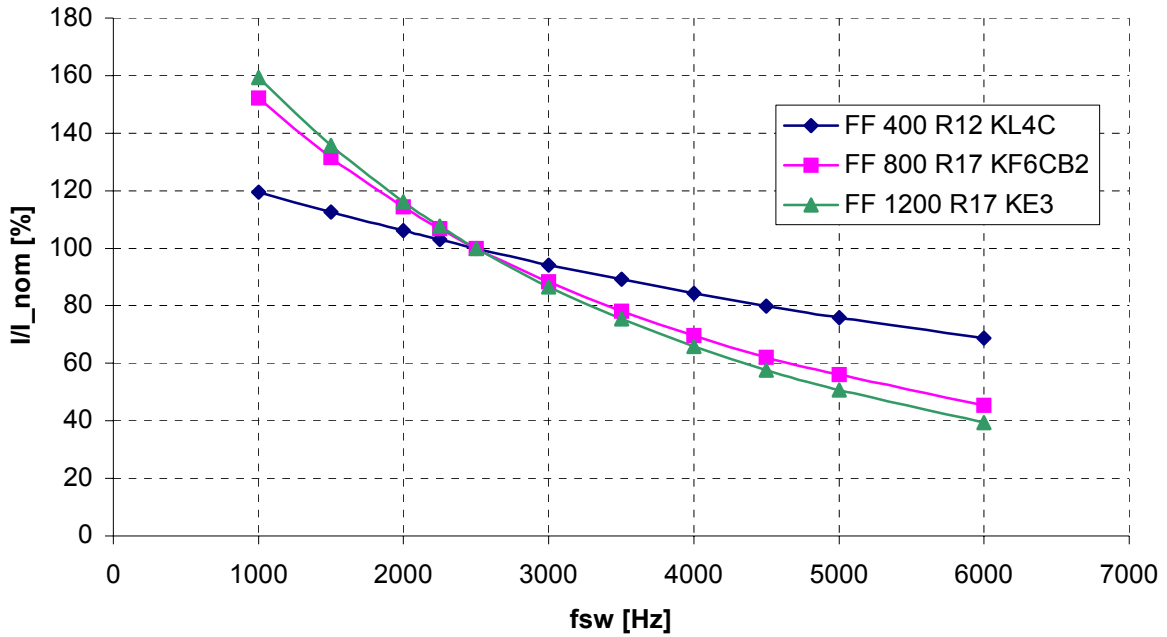


$\cos(\phi) = -0.64$, (generator)
 $\Theta_{air} = 40^\circ\text{C}$

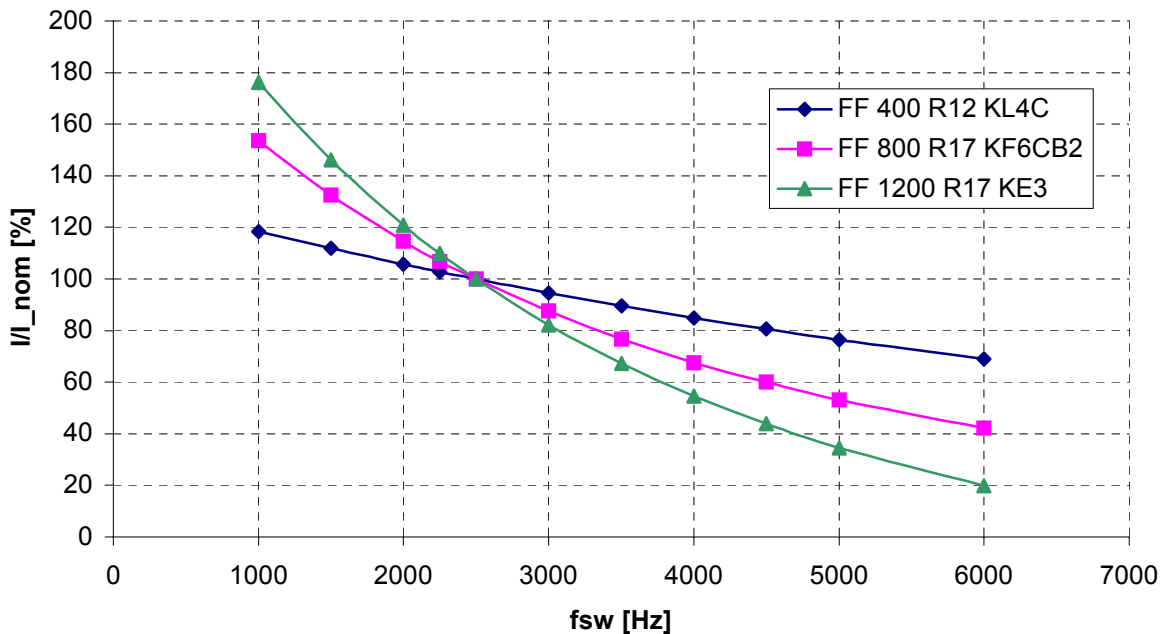


Current derating at different switching frequencies. See datatable for nominal switching frequency. In this drawing 2500Hz is assumed. **Maximal 100% current is allowed.**

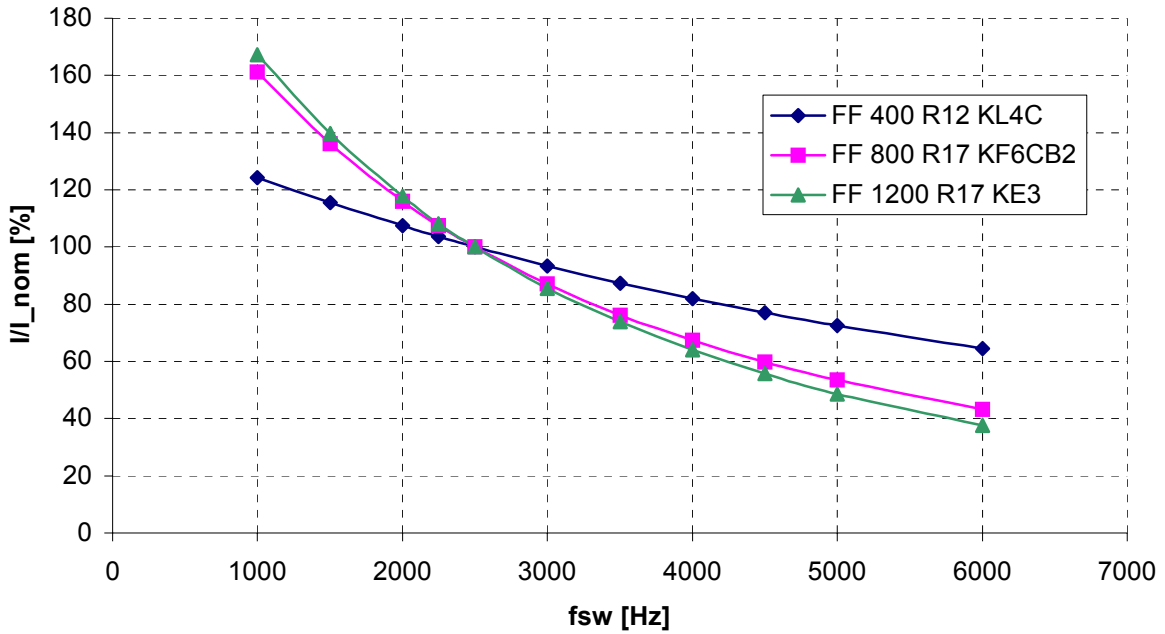
**IGBT, $\cos(\phi) = 0.64$
Theta_{air} = 40°C**



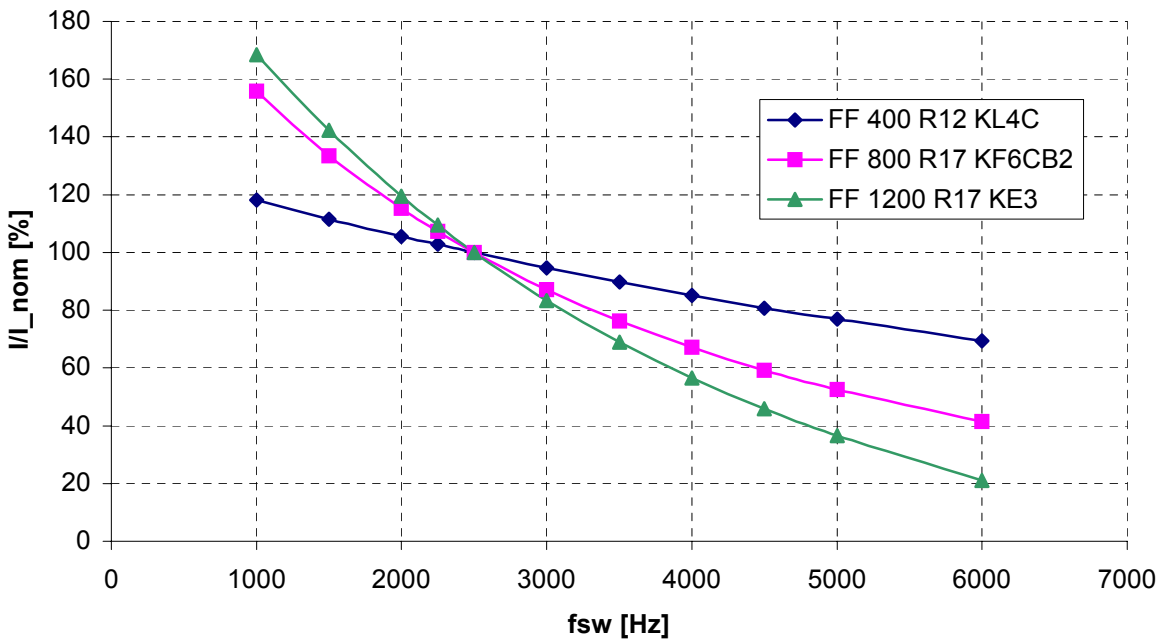
**Diode, $\cos(\phi) = 0.64$
Theta_{air} = 40°C**



**IGBT, $\cos(\phi) = -0.64$
 $\Theta_{air} = 40^\circ\text{C}$**



**Diode, $\cos(\phi) = -0.64$
 $\Theta_{air} = 40^\circ\text{C}$**



Miscellaneous

This technical information specifies semiconductor stacks but promises no characteristics. It is valid in combination with the belonging technical notes.

This document may be changed without prior notice.

Warning!

Prior to installation and commissioning all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and missing or damaged signs are replaced.

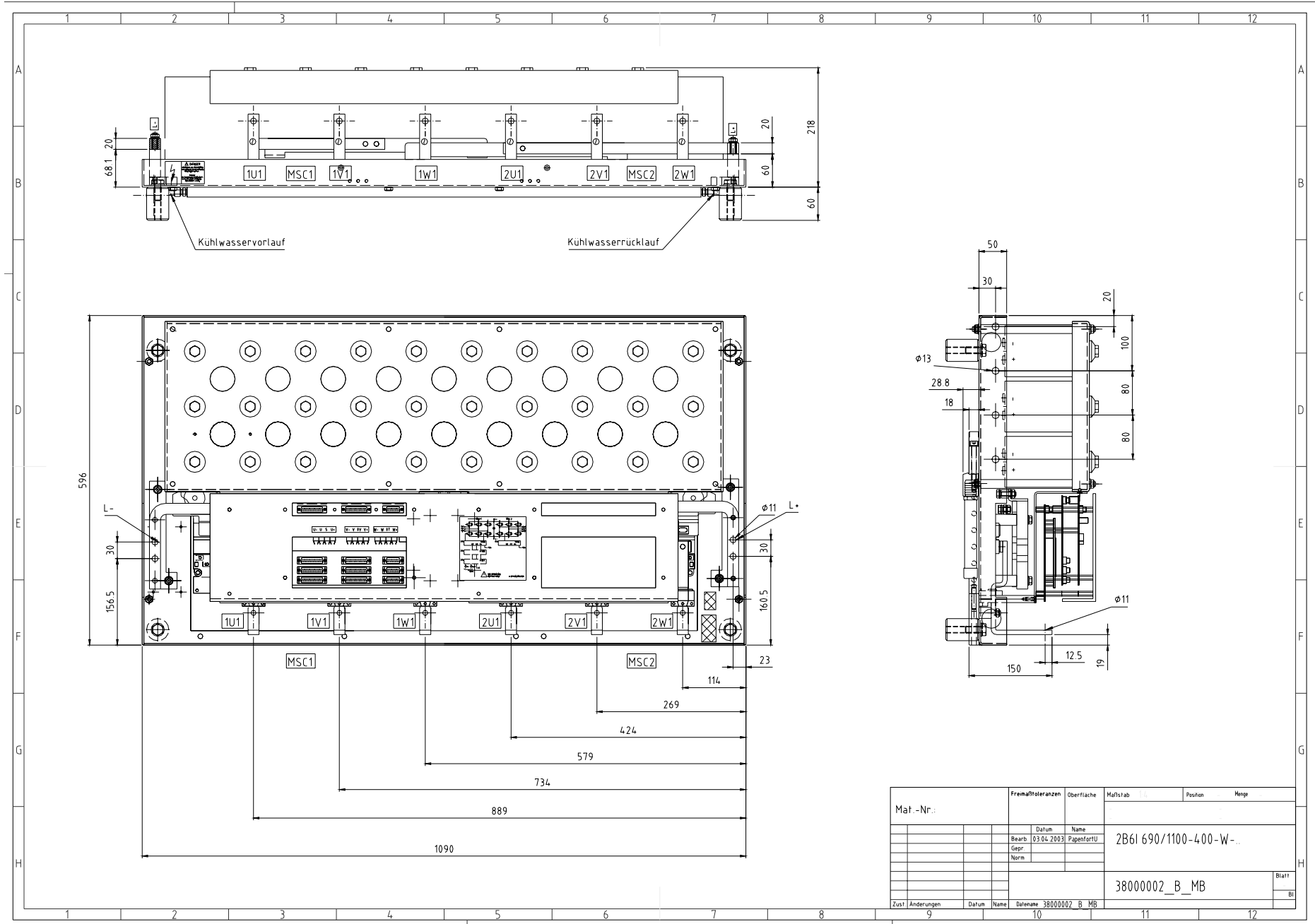
The safety instructions have to be strictly adhered to.

The manual contains detailed information on all technical topics with regard to the eupec ModSTACK. For further details regarding publications of the eupec ModSTACK and information on other publications in the area of ModSTACKs please contact your nearest eupec branch or visit our website: <http://www.eupec.com>.

©eupec GmbH 2003. All rights reserved

Prepared by	J.Schiele	2003-11-24	Date of publication	
Approved by	H.Nübel	2003-11-24	Revision	0

PowerSTACK 2B6I 500/800-450W, Drawing, Preliminary Data



Mat.-Nr.:	Freimaßtoleranzen	Oberfläche	Maßstab	Position	Henge
	Datum	Name			
	Bearb. 03.04.2003	Papenfertig	2B6I 690/1100-400-W...		
	Gepr.	Norm			
			38000002_B_MB		
Zust. Änderungen	Datum	Name	Datenname 38000002_B_MB		Blatt
					BI